

# Large Data Visualization on Distributed Memory Multi-GPU Clusters

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## Abstract

*Data sets of immense size are regularly generated on large scale computing resources. Even among more traditional methods for acquisition of volume data, such as MRI and CT scanners, data which is too large to be effectively visualized on standard workstations is now commonplace.*

*One solution to this problem is to employ a ‘visualization cluster,’ a small to medium scale cluster dedicated to performing visualization and analysis of massive data sets generated on larger scale supercomputers. These clusters are designed to fit a different need than traditional supercomputers, and therefore their design mandates different hardware choices, such as increased memory, and more recently, graphics processing units (GPUs). While there has been much previous work on distributed memory visualization as well as GPU visualization, there is a relative dearth of algorithms which effectively use GPUs at a large scale in a distributed memory environment. In this work, we study a common visualization technique in a GPU-accelerated, distributed memory setting, and present performance characteristics when scaling to extremely large data sets.*

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## 1. Introduction

Visualization and analysis algorithms, volume rendering in particular, require extensive compute power relative to data set size. One possible solution is to use the large scale supercomputer that generated the data, which clearly has the requisite compute power. But it can be difficult to reserve and obtain the computing resources required for viewing large data sets. An alternative approach, one explored in this paper, is to use a smaller scale cluster equipped with GPUs, which can provide the needed computational power at a fraction of the cost – provided the GPUs can be effectively utilized. As a result, a semi-recent trend has emerged to procure GPU-accelerated visualization clusters dedicated to processing the data generated by high end supercomputers; examples include ORNL’s Lens, Argonne’s Eureka, TACC’s Longhorn, SCI’s Tesla-based cluster, and LLNL’s Gauss.

Despite this trend, there have been relatively few efforts to study distributed memory, GPU-accelerated visualization algorithms which can effectively utilize the resources available on these clusters. In this paper, we report parallel vol-

ume rendering performance characteristics on large data sets for a typical machine of this type.

Our system is divided into three stages:

1. *An intelligent pre-partitioning* which is designed to make combining results from different nodes easy.
2. *A GPU volume renderer* to perform the per-frame volume rendering work at interactive rates.
3. *MPI-based compositing* based on a sort-last compositing framework.

Müller et al. presented a system similar to our own that was limited to smaller data sets [MSE06]. We have extended the ideas in that system to allow for larger data sets, by removing the restriction that a data set must fit in the combined texture memory of the GPU cluster and adding the ability to mix in CPU-based renderers, enabling us to analyze the parallel performance on extremely large data sets. The primary contribution of this paper is an increased understanding of the performance characteristics of a distributed memory GPU-accelerated volume rendering algorithm at a

scale (256 GPUs) much larger than previously published. Further, the results presented here (data sets up to  $8192^3$  voxels) represent some of the largest parallel volume renderings attempted thus far.

Our system and benchmarks allow us to explore issues such as:

- the balance between rendering and compositing, which is a well-studied issue with CPU-based rendering, but currently with unclear performance tradeoffs for rendering on GPU clusters;
- the overhead of transferring data to and from a GPU;
- the importance of process-level load balancing; and
- the viability of GPU clusters for rendering very large data.



**Figure 1:** Output of our volume rendering system with a data set representing a burning helium flame.

This paper is organized as follows: in section 2, we overview previous work in parallel compositing and GPU volume rendering. In section 3, we outline our system in detail. Section 4 discusses our benchmarks and presents their results. Finally, in section 5 we draw conclusions based on our findings.

## 2. Previous Work

Volume rendering in a serial context has been studied for many years. The basic algorithm [DCH88] was improved significantly by including empty space skipping and early ray termination [Lev90]. Max provides one of the earliest formal presentations of the complete volume rendering equation in [Max95]. Despite significant algorithmic advances from research such as [Lev90], the largest increase in performance for desktop volume renderers has come from taking advantage of the 3D texturing capabilities [CCF94, CN94, WE98] and programmable shaders [KW03] available on modern graphics hardware.

Extensive research has been done on parallel rendering

and parallel volume rendering. Much of this work has focused on achieving acceptable compositing times on large systems. Molnar et al. conveyed the theoretical underpinnings of parallel rendering performance [MCEF94]. Earlier systems for parallel volume rendering relied on direct send [Hsu93, MPH93], which divides the volume up into at least as many chunks as there are processors, sending ray segments (fragments) to a responsible tile node for compositing via the Porter and Duff *over* operator [PD84]. These algorithms are simple to implement and integrate into existing systems, but have sporadic compositing behavior and thus have the potential to exchange a large number of fragments, straining the network layers when scaling to large numbers of processors. Tree based algorithms feature more regular communication patterns, but impose an additional latency which may not be required, depending on the particular frame and data decomposition. Binary swap and derivative algorithms are a special case of tree-based algorithms that feature equitable distribution of the compositing workload [MPHK94]. Despite advancements in compositing algorithms, network traffic remains unevenly distributed in time, and thus high-performance networking remains a necessity for subsecond rendering times on large numbers of processors.

In the area of distributed memory parallel volume rendering of very large data sets, the algorithm described by Ma et al. in [MPHK93] has been taken to extreme scale in several followup publications. In [CDM06], data set sizes up to  $3000^3$  are studied using hundreds of cores. In this regime, the time spent ray casting far exceeds the composite time. In [PYRM08, PYR\*09], the data set sizes range up to  $4480^3$ , while core counts of tens of thousands are studied. In [HBC10], the benefits of hybrid parallelism are explored at concurrency ranges going above two hundred thousand cores. For both of these studies, when going to extreme concurrency, compositing time becomes large and dominates ray casting time. This suggests that a sweet spot may exist with GPU-accelerated distributed memory volume rendering. By using hardware acceleration, the long ray casting times encountered in [CDM06] can be overcome. Simultaneously, the emerging trend of composite-bound rendering time observed in [PYR\*09] and [HBC10] will be mitigated by the ability to use many fewer nodes to command the same compute power.

Numerous systems have been developed to enable parallel rendering in existing software. Among the most well-known is Chromium [HHN\*02], a rendering system which can transparently parallelize OpenGL-based applications. The Equalizer framework boasts multiple compositing strategies, including an improved direct send [EP07]. The IceT library provides parallel rendering with a variety of sort-last compositing strategies [MWP01].

There has been less previous work studying volume rendering on multiple GPUs. Strengert et al. developed a sys-

tem which used wavelet compression and adaptively decompressed the data on small GPU clusters [SMW\*04]. Marchesin et al. compared a volume that ran on two different two-GPU configurations: two GPUs on one system, and one GPU on two networked systems [MMD08]. The use of just one or two systems, coupled with an in-core renderer artificially constrained the data set size. Müller et al. developed a distributed memory volume renderer that runs on GPUs [MSE06]; their system differs from ours in a few key ways. First, we use an out-of-core volume renderer and therefore can exceed the available texture memory of the GPU by also utilizing the CPU memory. To further reduce memory costs, we compute gradients dynamically in the GLSL shader [KW03], obviating the need to upload a separate gradient texture. This also has the benefit of avoiding a pre-process step, which is normally software-based in existing general-purpose visualization applications (including the one we chose to implement our system within) and can be quite time consuming for large data sets. Further differentiating our system and in line with recent trends in visualization cluster architectures, we enable the use of multiple GPUs per node. Müller et al. used a direct send compositing strategy ([Hsu93,MPHK93]), whereas we use a tree-based compositing method ([MWP01]). Finally, and most importantly, we report performance results for substantially more GPUs and much larger data sets, detailing the scalability of GPU-based visualization clusters. We therefore believe our work is the first to evaluate the usability of distributed memory GPU clusters for this scale of data.

### 3. Architecture

We implemented our remote rendering system inside of VisIt [CBB\*05], which is capable of rendering data in parallel on remote machines. The system is comprised of a lightweight ‘viewer’ client application, connected over TCP to a server which employs GPU cluster nodes. All rendering is performed on the cluster, composited via MPI, and images, optionally compressed via zlib, are sent back to the viewer for display. Example output from our system is in Figure 1.

Although VisIt provided a good starting point for our work, we needed to make significant changes in order to implement our system. In this section, we highlight the main features of our system, taking special care to note where we have deviated from existing VisIt functionality.

#### 3.1. Additions to VisIt

##### 3.1.1. Multi-GPU Access

At the outset, VisIt’s parallel server supported only a single GPU per node. We have revamped the manner in which VisIt accesses GPUs to allow the system to take advantage of multi-GPU nodes. When utilizing GPU-based rendering, each GPU is matched to a CPU core which feeds data to

that GPU. Additionally, when the number of CPU cores exceeds the number of available GPUs, we allow for the use of software-based renderers on the extra CPUs. This code has been contributed to the VisIt project.

##### 3.1.2. Partitioning

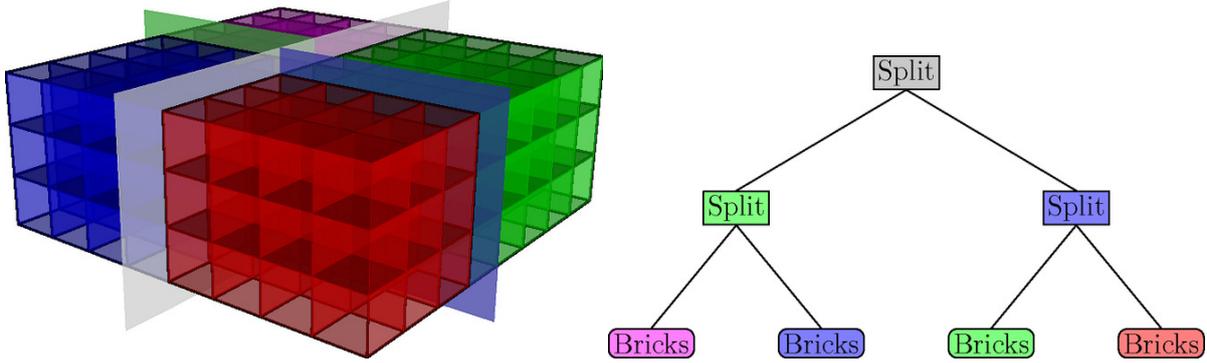
VisIt contained a number of load decomposition strategies prior to our work. However, we found these strategies to be insufficient for a variety of reasons:

- **Brick-based** Equalizing the distribution of work in VisIt was entirely based on *bricks*, or pieces of the larger data set. Our balancing algorithms use the time taken to render the previous frame to determine a weighted distribution of loads.
- **Master-slave** Dynamic balance algorithms in VisIt are based on a *master* node, which tells slaves to process a brick, waits for completion, and then sends slaves a new brick to process. We implemented a flat hierarchy, as seems to be more common in recent literature [MMD06, MSE06].
- **Compositing** *Most importantly*, for our object-based decomposition to work correctly, we needed a defined ordering to perform correct compositing. The load balancing and compositing subsystems were independent prior to our work.

Our system relies on a *kd*-tree for distributing and balancing the data. The spatial partitioning is done once initially and can be adaptively refined by the rendering times from previous frames. The initial tree only considers the number of bricks available in the data set, and attempts to evenly distribute them among processes, to the extent that is possible. When using static load balancing, this decomposition is determined and invariant for the life of the parallel job. Figure 2 depicts a possible configuration determined by the partitioner, and shows the corresponding *kd*-tree.

When the dynamic load balancer is enabled, we use the last rendering time on each process to determine the next configuration. In our initial implementation, the metric we utilized was the total pipeline execution time to complete a frame. This included the time to read data from the disk, as well as compositing time, among other inputs. However, we found that I/O would dwarf the actual rendering time. Further, compositing time is not dependent on the distribution of bricks. This therefore proved to be a poor metric. Switching the balancer to use the total render time for all bricks on that process gave significantly better results.

In order to compare different implementations, we implemented multiple load balancing algorithms, notably those described in Marchesin et al. and Müller et al.’s work [MMD06, MSE06], in order to compare different implementations. In both cases, leaf nodes represent processes, and each process has some number of bricks assigned to it. In the Marchesin-based approach, we start at the parents of



**Figure 2:** Decomposition and corresponding *kd*-tree for an  $8 \times 8 \times 3$  grid of bricks divided among 4 processors. Adjacent bricks are kept together for efficient rendering and compositing. A composite order is derived dynamically from the camera location in relation to splitting planes. Note that the number of leaves in the tree is equal to the number of processes in the parallel rendering job.

the leaf nodes and work our way up the tree, searching for imbalance among siblings. If two siblings are found to be imbalanced, a single layer of bricks is moved along the splitting plane. This process continues up to the root of the tree, at which time the virtual results are committed and the new tree dictates the resulting data distribution. In the Müller-based approach, we begin with the root node and use pre-order traversal to find imbalance among siblings. Once imbalance is found, the process stops for the current frame. Instead of blindly shifting a layer of blocks between the siblings, the method derives the average rendering cost associated with a layer of bricks along the split plane, and shifts this layer if the new configuration would improve rendering times.

In addition to achieving a relatively even balance among the data, the *kd*-tree is used in the final stages to derive a valid sort-last compositing order.

### 3.1.3. Rendering

Rendering is performed in parallel on all nodes. We integrated a library for both slice-based and ray-based direct volume rendering via GLSL shaders; for this work, we used the slice-based renderer. For nodes without access to a GPU, data are rendered through the Mesa library’s ‘swrast’ module, which executes vertex and fragment shaders on the CPU. Nodes render all the data they are responsible for independently, without regard for the screen space projection of the data.

Data are forwarded “as-is” from disk, without modification or transformation to its type. In our experiments, this means that floating point data flows all the way through the pipeline, and becomes the input to the renderers – we push the native-precision data down to the GPU and render it at full resolution. Of course, data are effectively quantized due to the limited resolution of a transfer function.

We find this architecture compelling because it removes any need to pre-process the data. VisIt’s parallel pipeline execution is based wholly around the bricks given as input to the tool. Our main restriction is the size of each individual brick: since we utilize an out-of-core volume renderer, we can stream sets of bricks through a GPU, even if the stream exceeds the maximum 3D texture size or GPU memory available. However, each individual brick must be small enough to fit within the texture memory available on a GPU.

In practice, this limitation has not affected how we generated or visualized the data for this work. Should the need arise, we could re-brick the data set to sizes more amenable for visualization.

### 3.1.4. Compositing

After rendering completes, each node has a full image with a subset of the total data volume rendered into it. A compositing step takes these partial images and combines them to produce the final result. As noted in Section 2, we did not expect compositing to dictate the performance of our system; still, we had previously integrated the IceT parallel compositing library [MWP01], for its ease of integration and proven results. IceT gives considerably better performance than the other built-in compositors available, and so we extended the compositing code to derive a compositing order from the *kd*-tree and hand it to IceT.

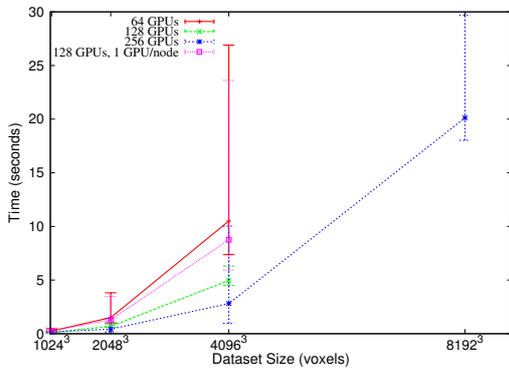
IceT implements a number of different compositing modes. However, not all of them support what IceT calls *ordered compositing*, as is needed for object-parallel distributed volume rendering. For this work, we have utilized the so-called *reduce* strategy, which, since we only configure a single ‘tile’ in our system, essentially simplifies [MWP01] to an implementation of Binary Swap [MPHK94].

#### 4. Evaluation

We implemented and tested our system on *Lens*, a GPU-accelerated visualization cluster housed at ORNL. However, we were only able to access 16 GPUs on that machine. In order to access a larger number of GPUs, we transitioned to *Longhorn*, a larger cluster housed at the Texas Advanced Computing Cluster. Specifications for each cluster are listed in Table 1. Due to machine availability and configuration, we were not able to fully utilize either machine.

##### 4.1. Rendering Times

The two dominant factors in distributed memory visualization performance are the time taken to render the data and the time taken to composite the resulting sub-images. These have the largest impact on usability, because they comprise the majority of the latency a user experiences: the time between when the user interacts with the data and when the results of that interaction are displayed.

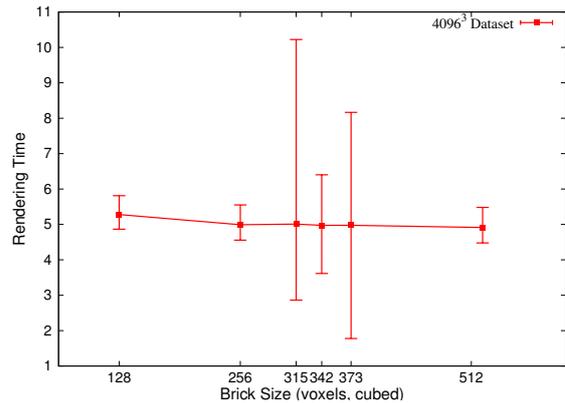


**Figure 3:** Overall rendering time when rendering to a  $1024 \times 768$  viewport on *Longhorn*. This incorporates both rendering and compositing, and therefore shows the delay a user would experience if they used the system on a local network. Data points are the average across many frames, and error bars indicate the rendering times for the slowest and quickest frames, respectively. For these results we used a domain consisting of  $13^3$  bricks (varying brick size), with the exceptions that all runs in the 128 GPU cases used  $8^3$  bricks, and the run for the  $8192^3$  data set was done using  $32^3$  bricks.

Our data originated from a simulation performed by the Center for Simulation of Accidental Fires and Explosions (C-SAFE), designed to study the instabilities in a burning helium flame. In order to study performance at varying resolutions, we resampled this data to  $1024^3$ ,  $2048^3$ ,  $4096^3$ , and  $8192^3$ , at a variety of brick sizes. We then performed tests, varying data resolution, image resolution, choice of brick

size, and number of GPUs, up to 256. Unless noted otherwise, we divided the data into a grid of  $8 \times 8 \times 8$  bricks for parallel processing (larger data sets used larger bricks), and rendered into a  $1024 \times 768$  viewport.

Figure 3 shows the scalability on the *Longhorn* cluster. The principal input which affects rendering time is the data set size, as one might expect. These runs were all done using 2 GPUs per node, except the "128 GPUs, 1 GPU/node" case, which was run on 128 nodes, each accessing a single GPU. With very large data, there is a modest increase in performance for this experimental setup.



**Figure 4:** Rendering time as a function of brick size. Error bars indicate the minimum and maximum times recorded, across all nodes, for that particular brick size; high disparity indicates the rendering time per-brick was highly variable, and load imbalance was therefore likely. All tests were done with a  $4096^3$  data set statically load balanced across 128 GPUs on 64 nodes, using a scripted camera which requested the same viewpoints each run. Note that the choice of brick size matters little in the average case, but bricks using non-power-of-two sizes give widely varying performance. Brick sizes of  $512^3$  technically give the best performance, though raw data show it is only hundredths of a second faster than  $256^3$  bricks.

As can be seen in Figure 4, the brick size, generally, has little impact in performance. A parallel volume renderer's performance is dictated by the slowest component though, and therefore the average rendering time is less important than the maximum rendering time. Taking that into account, it is clear that brick sizes which are not a power of two are poor choices. Dropping down to  $128^3$ , we can see that per-brick overhead begins to become more noticeable, impacting overall rendering times. We found larger brick sizes of  $512^3$  get the absolute best performance, with  $256^3$  a good choice as well, as the differences are minor enough that they may be considered sampling error. Of course, such recommendations may be specific to the GPUs used in *Longhorn*.

We were initially surprised to find that the image resolu-

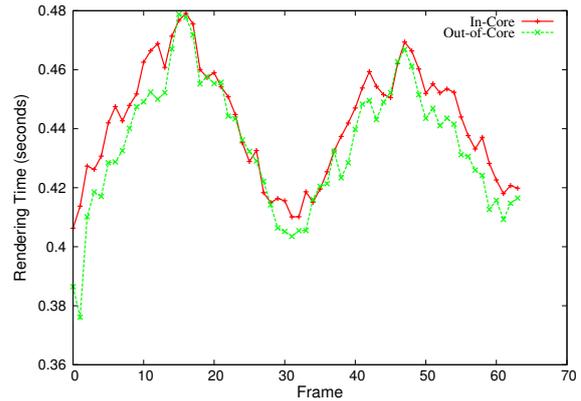
| Component       | <i>Lens</i>      | <i>Longhorn</i>         |
|-----------------|------------------|-------------------------|
| Number of nodes | 32               | 256                     |
| GPUs per node   | 2                | 2                       |
| Cores per node  | 16               | 8                       |
| Graphics card   | NVIDIA 8800 GTX  | NVIDIA FX 5800          |
| Per-node Memory | 64 GB            | 48 GB                   |
| Processors      | 2.3 GHz Opterons | 2.53 GHz Nehalems       |
| Interconnect    | DDR Infiniband   | Mellanox QDR InfiniBand |

**Table 1:** Configuration of GPU clusters utilized.

tion, while relevant, was not a significant factor in the overall rendering time. When developing single GPU applications that run on a user’s desktop, our experience was the opposite: that image size did play a significant role in performance. We at first thought this was due to skipping bricks which were ‘empty’ under our transfer function – our domain is perfectly cubic, yet as is displayed in Figure 1, very little of the domain is actually visible – but even after changing to a transfer function with no “0” values in the opacity map, rendering times changed very little. We concluded that the data sizes are so large compared to the number of pixels rendered that the image size is not relevant as a factor.

In our initial implementation on *Lens*, we noticed that we began to strain the memory allocators while rendering a  $3000^3$  data set, as we approached low memory conditions. Our volume renderer automatically accounts for low memory conditions and attempts to free unused bricks before failing outright. However, an operating system will thrash excessively before finally deciding to fail an allocation, and therefore during the time leading up to a failed allocation, performance will drop considerably. Worse, we are working in a large existing code base, and attempting to manage allocations outside our own subsystem would prove unwieldy. As such, we found the original scheme to be unstable; the rendering system would create memory pressure, causing other subsystems to fail an allocation in areas where it may be difficult or impossible to ask our volume renderer to free up memory.

To solve this problem, we render the data in a true out-of-core fashion: bricks are given to the renderer, rendered into a framebuffer object, and immediately thrown away. We might expect that out-of-core algorithms would have more per-block overhead and therefore be slower than an in-core algorithm. As shown in Figure 5, out-of-core approach actually out-performs the analogous in-core approach even when there is sufficient memory to hold the data set. In this case, lookups were performed into a data structure with logarithmic lookup time; the in-core approach did these lookups in logarithmic time, whereas the conservative approach taken in the out-of-core algorithm meant the container maxed out at one element, which accounted for the very minor improvement to performance.



**Figure 5:** Rendering times, per frame, for the in-core and out-of-core approaches to rendering a  $1024^3$  data set (which fits comfortably in memory) across 16 GPUs. Additional processing in the out-of-core case does not negatively impact performance.

#### 4.1.1. Readback and Compositing

In earlier results, particularly with GPU-based rendering architectures, the community was generally concerned with the time required to read the resulting image data from the GPU and into the host’s memory [MMD08]. Our study did not provide corroboration of this concern, which we interpret as a positive data point with respect to evolving graphics subsystems. Our system did demonstrate that this time increased as the resolution grew, but as can be seen in Table 2, even at  $1024 \times 768$  this step took only thousandths of a second.

As expected, the time required for image composition is significantly reduced when taking advantage of the GPUs available in a visualization cluster. Since a GPU can render much faster than a software-based renderer, one can achieve acceptable rendering performance using far fewer nodes. Furthermore, because compositing scales with the number of nodes involved in the compositing process, compositing performance improves significantly when utilizing fewer nodes.

| Dataset Size      | Rendering (s) | Readback (s) | Compositing (s) | Total (s) |
|-------------------|---------------|--------------|-----------------|-----------|
| 1024 <sup>3</sup> | 0.06141       | 0.00328      | 0.06141         | 0.12610   |
| 2048 <sup>3</sup> | 0.35107       | 0.00377      | 0.07673         | 0.43157   |
| 4096 <sup>3</sup> | 2.50984       | 0.00377      | 0.29533         | 2.80894   |
| 8192 <sup>3</sup> | 19.60648      | 0.00373      | 0.51799         | 20.12820  |

**Table 2:** Breakdown of the different pipeline stages for various data set sizes, when running on 256 GPUs and rendering into a 1024x768 viewport. All times are in seconds. The 1024<sup>3</sup>, 2048<sup>3</sup>, and 4096<sup>3</sup> case used 13<sup>3</sup> bricks (varying brick size); the 8192<sup>3</sup> case used 32<sup>3</sup> bricks, making each brick 256<sup>3</sup>. Compositing time rises only artificially; if a node finishes rendering before other nodes, the time it must wait was included under ‘Compositing’ due to an artifact of our sampling code. Thus, the data imply that larger data sets see more load imbalance.

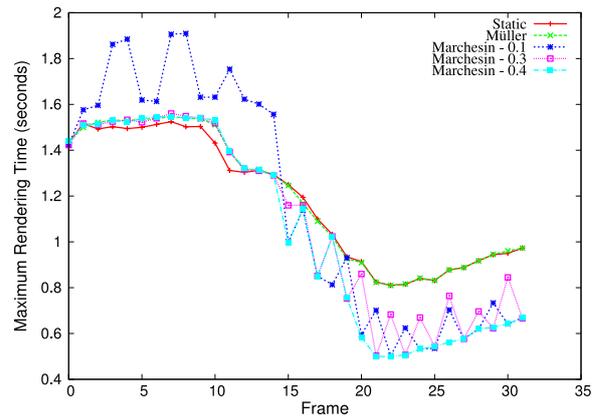
## 4.2. Load Balancing

We also sought to examine the utility of load balancing algorithms for our system. We have implemented the algorithms as presented in two recent parallel volume rendering papers, and compared rendering times to each other and to a statically balanced case. Figure 6 illustrates the comparisons, where the times shown are the maximum on all processes.

We did a variety of experiments with multiple load balancer implementations, using 8 or 16 GPUs. Our initial fly-through sequence proved to be inappropriate for the application of a load balancer, as there was not enough imbalance in the system to observe a significant benefit. We then attempted to zoom out of the dataset, but rendering times increasing on all nodes was not a case the balancers we implemented could effectively deal with: we found many cases where the balancers would shift data to a node that was previously idle or at least doing very little work, and a frame or two later the workload on such nodes would spike. This occurred because these nodes had both 1) received new data as part of the balance and, 2) retained old data as part of the initial decomposition or older balancing processes. The sudden additional workload of previously invisible bricks caused these nodes to overcompensate, sending data to other “idle” nodes – nodes which would experience the same problem a frame or two later.

In previous work, authors have praised the effect load balancing has when zooming *in* to a data set. This naturally creates imbalance, as some nodes end up with data which are not rendered under the current camera configuration, and therefore the node has no work to do.

With the implementations we recreated as faithfully as possible, we did find that zooming in to the data set was a task that was well-suited for load balancing. Still, we encountered issues even with this case. For the algorithm given in [MMD06], we observed that data would move back and forth between nodes quite frequently, having a negative impact on overall rendering time. We therefore introduced a ‘threshold’ parameter to the existing algorithm, in an attempt to limit this ‘ping-pong’ behavior. As we move up the tree, imbalance between the left and right subtrees is subject to this threshold; if it does not exceed the threshold, the imbal-

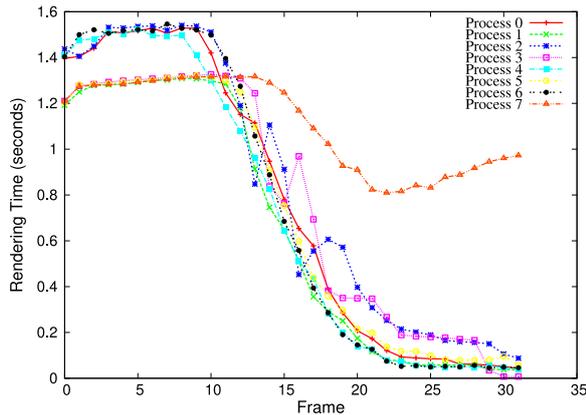


**Figure 6:** The maximum rendering time across all nodes under various balancing algorithms. The numbers after some algorithms indicate thresholds: rendering disparity under these thresholds is ignored.

ance is ignored. This is a very useful parameter for ensuring that we do not move data too eagerly. Generally, setting this threshold too high will yield behavior equivalent to the static case; setting it to low leads to a considerable amount of unnecessary data shifting, and we found that this in many cases overcompensated for minor, expected variations (such as those one might expect from differing brick sizes; see Figure 4). For example, see Figure 6, in which low thresholds display an obvious ‘ping-pong’ effect as nodes overcompensate for increased rendering load.

Müller et al. describe a different balancing system [MSE06]. This system calculates the average cost of rendering a brick, and therefore has a clearer idea of what the effect of moving a given set of bricks will have on overall system performance. Further, they introduce additional parameters which add some hysteresis to the system, which can help reduce the ‘ping-pong’ effect of nodes sending data to a neighbor, just to receive in the next frame when the neighbor becomes overloaded.

We found that this algorithm did do intelligent balancing for reasonable settings of these parameters, and the addi-



**Figure 7:** Per-process rendering times for the ‘Müller’ line given in Figure 6.

tional parameters could be successfully used to reduce excess data reorganization. Still, we found two issues with the approach: for one, the assumption that ‘all bricks are equal’ did not pan out for our work. Even assuming uniform bricks for a data set (true for our case, but likely not in a general system), one can see in Figure 4 that the time to render a brick sees variation on the order of a second. Secondly, despite experimenting with parameter settings, we found it difficult to get the algorithm to choose the ‘best’ set of nodes for balancing. In many cases, we found a particular node was an outlier, consistently taking the most time to render per frame. Yet it was common for this algorithm to balance different nodes. While rendering times would generally improve, the system’s performance is determined by the slowest node, and therefore making the fast nodes faster does not help overall performance.

This was apparent in the tests described in 6: the algorithm quite clearly balanced between some of the nodes, but the slowest node was never balanced, and therefore the user-visible performance for this run was equivalent to the static case. Figure 7 shows a more detailed analysis of the execution of the Müller algorithm that generated the data for Figure 6. The per-node rendering times in Figure 7 show that process 7 is usually the last process to finish and is often much slower than the next to the last. As evident from the lack of sudden discontinuities in that process’ rendering times, however, no bricks from process 7 move to other nodes. Therefore rendering times decrease but the maximum rendering time does not change.

We theorize that additions to the algorithm to learn weights for each individual brick would yield fruitful results. Furthermore, the algorithm explicitly attempts to avoid visiting the entire tree, as an attempt to bound the maximum time needed to determine a new balancing. In our work, we did not observe cases where iterating through nodes in the tree had a measurable impact on performance, and feel that

by doing so the algorithm could obtain the global knowledge it needs to balance data effectively. Both of these extensions are left to future work.

In Section 1, we noted a variety of questions which the design of our system allows us to answer.

- *Rendering vs. Compositing.* As shown in Table 2, sub-second rendering times are achieved using a very small number of nodes, relative to previous work. This relieves a significant source of work for compositing algorithms.
- *Overhead of GPU Transfer.* Table 2 shows readback time to be on the order of thousandths of a second for common image sizes. Measuring texture upload rates is difficult with the asynchronous nature of current drivers and OpenGL, but we did not find evidence to suggest this was a bottleneck.
- *Importance of Load Balancing.* A dynamic load balancer can have a very worthwhile impact on performance. However, it can also lower the performance of the system. Load balancers generally come with some number of tunable parameters, and useful settings for these parameters are difficult to determine a priori, and likely impossible for an end-user to effectively set. We observed that dynamic load balancing for volume rendering struggled in some of the cases often encountered in real world environments and, for this reason, believe there is still a gap before deploying these techniques in *production* systems. We see a great opportunity for future work in this area.
- *Viability.* As displayed mostly by Figure 3 and Table 2, rendering extremely large data sizes – up to  $8192^3$  voxels – is possible on relatively few nodes. Further, data sets up to  $2048^3$  can be rendered at approximately two frames per second.

## 5. Conclusions

With this study, we demonstrated that GPU accelerated rendering provides compelling performance for large scale data sets. Figure 3 demonstrates our system rendering data sets which are among some of the largest reported thus far, using far fewer nodes than previous work. This work shows that a multi-GPU node is a great foundational ‘building block’ to compose larger systems capable of rendering very large data. As the performance-price ratio of a GPU is higher (provided it can effectively parallelize the workload) than CPU-based solutions, this work makes the case for spending more visualization supercomputing capital on hardware acceleration, and acquiring smaller yet more performant clusters.

Reports on the time taken for various pipeline stages demonstrate that PCI-E bus speeds are fast enough that readback performance is not as great of a concern as it was a few years ago. However, it remains to be seen if contention will become an issue if individual nodes are made ‘fatter’, utilizing additional GPUs. The 1 GPU per node results given in Figure 3 suggests that multiple GPUs do contend for re-

sources, but at this scale the differences are not yet significant enough to warrant moving away from the more cost-effective ‘fat’ node architecture. Given the relatively few nodes needed for good performance on large data, and external work scaling compositing workloads out to tens of thousands of cores, it seems likely that the relatively ‘thin’ 2-GPU-per-system architecture can be made to scale to even larger systems.

### 5.1. Future Work

We would like to study our system with higher image resolutions, such as those available on a display wall, and larger numbers of GPUs. At some point, we expect compositing to become a significant factor in the amount of time needed to volume render large data, but we have not approached the cross-over point in this work, due to the use of ‘desktop’ image resolutions and low numbers of cores.

Our system allows substituting a Mesa-based software renderer when a GPU is not available. This provided a convenient means of implementation for an existing large software system, in particular because it allows pipeline execution to proceed unmodified through the rendering and compositing stages. However, tests very quickly showed that using software renderers when a GPU was available was not worthwhile, and usually ended up hurting performance more than helping. Therefore, we traded access to more cores for the guarantee that we will obtain GPUs for each core we do get.

An alternate system architecture would be to decouple the rendering process from the other work involved in visualization and analysis, such as data I/O, processing, and other pipeline execution steps. In this architecture, all nodes would read and process data, but processed, visualizable data would be forwarded to a subset of nodes for rendering and compositing. The advantage gained is the ability to tailor the available parallelism to the visualization tasks of data processing and rendering, which, as we have found, can benefit from vastly different parallel decompositions. The disadvantages are the overhead of data redistribution, and the wasted resources that arise from allowing non-GPU processes to sit idle while rendering.

Our compositing algorithm assumes that the images from individual processors can be ordered in a back-to-front fashion to generate the correct image. For this paper, we met this requirement by using regular grids, which are easy to load balance in this manner. It should be possible to also handle certain types of curvilinear grids and perhaps AMR grids. Extensions to handle unstructured grids would be difficult, but represent an interesting future direction.

Load balancing is an extremely difficult problem, and we have just scratched the surface here. The principal difficulty in load balancing is identifying good parameters to control how often and to what extent the balancing occurs. We

would like to see ideas and algorithms which move in the direction of user-friendliness: determining the most relevant parameters and deriving appropriate values for them automatically.

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